

DOMAIN POWER
NOTIFICATION SYSTEM

BACKGROUND OF THE INVENTION

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1. Field of the Invention.

The present invention relates to a semiconductor integrated circuit with multiple power domains and, more particularly, to a domain power notification system that prevents erroneous data from being unknowingly received by one domain when another domain experiences a power condition.

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2. Description of the Related Art.

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In a semiconductor integrated circuit that has multiple power domains that communicate with each other, an interruption in the power supply of a transmitting domain can lead to invalid data being received by a receiving domain. When the receiving domain is unaware that the transmitting domain is experiencing a power interruption, the receiving domain can unknowingly receive erroneous signals from the transmitting domain or random signals from the transmission line. In addition to interruptions in the power supply, a low-voltage condition in the transmitting domain can also lead to invalid data being unknowingly received by the receiving domain.

For example, in a simple two-domain case, a semiconductor integrated circuit can have a 3.3V analog receive domain that receives and digitizes analog signals, and a 1.8V digital processing domain that processes the digitized signals from the analog receive domain. A lost

power or low-voltage condition in the analog receive domain can lead to the digital processing domain unknowingly receiving invalid data.

Thus, there is a need for a technique that prevents a domain from unknowingly receiving invalid data when another domain loses
5 power or enters a low-voltage condition.

SUMMARY OF THE INVENTION

The present invention provides a domain power notification
10 system that detects when a domain experiences a power condition, and communicates that information to the domains that communicate with the problem domain. The effected domains stop communicating with the problem domain without passing erroneous information. The power conditions can include, for example, lost power and low-voltage
15 conditions.

A domain power notification system in accordance with the present invention includes a plurality of power domains that receive a corresponding plurality of status signals, and output a corresponding plurality of power signals so that each power domain receives a
20 corresponding status signal and outputs a corresponding power signal. Each domain is independently powered. The power signals represent power supply voltages on the power domains so that each power signal represents a power supply voltage on a power domain. Each power domain is connected to at least one other power domain.

25 The domain power notification system also includes a domain notification circuit that is connected to a first domain of the plurality of power domains. The domain notification circuit is independently-powered, receives a power signal from the first domain and a second

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power domain, and outputs a status signal to the first domain. The status signal indicates a power condition of the second power domain.

The present invention also includes a method of operating the domain notification circuit. In accordance with the present invention,

5 the method includes the steps of monitoring a power signal from the second power domain and outputting a status signal to indicate when the power signal from the second power domain falls below a minimum operational voltage.

A better understanding of the features and advantages of the
10 present invention will be obtained by reference to the following detailed description and accompanying drawings that set forth an illustrative embodiment in which the principles of the invention are utilized.

BRIEF DESCRIPTION OF THE DRAWINGS

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FIG. 1 is a block diagram illustrating a domain power notification system 100 in accordance with the present invention.

FIG. 2 is a block diagram illustrating a domain power notification system 200 in accordance with an alternate embodiment of the present
20 invention.

DETAILED DESCRIPTION

FIG. 1 shows a block diagram that illustrates a domain power
25 notification system 100 in accordance with the present invention. As described in greater detail below, the present invention detects when a power domain experiences a power condition, such as lost power and low-voltage power, and communicates that information to the domains

that communicate with the problem domain. The effected domains then stop communicating with the problem domain without passing erroneous information.

In the example shown in FIG. 1, system 100 includes a series of

5 independently-powered domains DM1-DMn that are connected to an independently-powered domain notification circuit 110. Domains DM1-DMn output a corresponding series of individual power supply signals PS1-PSn to domain notification circuit 110, and receive a series of domain status signals DS1-DSn and a series of power on reset signals

10 PR1-PRn from domain notification circuit 110.

The individual power supply signals PS1-PSn in this example are the power supply voltages of the domains. As a result, the power supply signals PS1-PSn track changes in the power supply voltages of the domains. For example, if first domain DM1 is connected to an

15 energized 3.3V power supply that is properly functioning, then power supply signal PS1 is 3.3V.

If the power supply voltage on the domain drops to two volts, then power supply signal PS1 drops to two volts. Similarly, if second domain DM2 is connected to an energized 12-volt power supply that is

20 properly functioning, then power supply signal PS2 is 12 volts. If the power supply voltage rises to 15 volts, then power supply signal PS2 rises to 15 volts.

In addition, each domain DM includes internal logic that responds to the domain status signal DS and the power on reset signal PR.

25 Further, each domain DM is connected to at least one other domain via a communication bridge 112. Communication bridge 112 can be implemented with a number of serial connections between the domains,

a parallel connection to the domains via a bus, or a combination of the two.

Domain notification circuit 110 receives a number of signals, including a reset signal RST. Reset signal RST can be used to reset 5 circuit 110 and/or one or more domains DM via the power on reset signals PR. Domain notification circuit 110 also includes a low-voltage detection (LVD) circuit 112 that receives each of the individual power supply signals PS1-PSn, and detects when the voltage of a power supply signal PS rises above and falls below a minimum operating level for that 10 power supply signal PS.

In addition, LVD circuit 112 outputs the domain status signals DS1-DSn which indicate the power condition of domains DM1-DMn. The domain status signals DS1-DSn can each include the same information, or be customized. For example, the domain status signal DS1 output to 15 first domain DM1 can be customized to change only if the power supply voltage of a domain that transmits data to, or receives data from, first domain DM1 rises above or falls below the minimum operating level of the domain.

In addition, low-voltage detection circuit 112 can also include a 20 signal filter 114 that removes the effect of voltage spikes, thereby preventing low-voltage detection circuit 112 from responding to transient power supply conditions that do not effect the operation of a domain.

In operation, domain notification circuit 110 is a part of the first 25 or one of the first domains to power up, such as the input/output power domain of a semiconductor integrated circuit. After powering up, domain notification circuit 110 monitors the power supply voltages provided to domains DM1-DMn via the individual power supply signals

PS1-PSn, and detects when the power supply voltage provided to a domain rises above the minimum operational voltage of the domain.

When domain notification circuit 110 detects via an individual power supply signal PS that the power supply voltage provided to a domain is above the minimum operational voltage of the domain, circuit 110 outputs the corresponding power on reset signal PR. In response to the power on reset signal PR, the domain powers up following a predefined sequence. Thus, circuit 110 insures that stable power in excess of the minimum has been provided to a domain before the domain is powered up.

For example, when the power supply voltage on first domain DM1 rises above the minimum operational voltage of first domain DM1, domain notification circuit 110 outputs power on reset signal PR1 to first domain DM1. In response to power on reset signal PR1, first domain DM1 powers up following a predefined sequence.

In addition, a predetermined time after outputting the power on reset signal, domain notification circuit 110 modifies the domain status signals DS1-DSn to indicate that a domain has powered up. Following this, circuit 110 outputs the domain status signals DS1-DSn to each powered up domain DM, or to each relevant powered up domain in the case of customized domain status signals DS. Thus, once a domain powers up, the powered-up domains are informed of the other domains that are powered up and ready to communicate.

For example, a predetermined time after first domain DM1 receives power on reset signal PR1, first domain DM1 receives first domain status signal DS1 from domain notification circuit 110. Domain status signal DS1, in turn, indicates which other of the domains DM2-DMn are powered up and ready to communicate with first domain DM1.

In addition to monitoring the power on reset sequence, or alternately from monitoring the reset sequence (each domain DM can alternately include a power on reset circuit that detects a stable voltage above the minimum and issues the reset command as well as receive a direct reset signal rather than via circuit 110), domain notification circuit 110 also monitors the domains DM1-DMn to detect low voltage and lost power conditions.

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When domain notification circuit 110 detects via an individual power supply signal PS that the power supply voltage provided to a domain DM has fallen below the minimum operational voltage of the domain DM, circuit 110 modifies the domain status signals DS1-DSn to indicate that the domain DM has fallen below the minimum required level.

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Following this, circuit 110 outputs the domain status signals DS1-DSn to each powered up domain DM, or to each relevant powered up domain in the case of customized domain status signals DS. Thus, once a domain loses power, the other domains that are powered up are notified of this situation.

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In response to the notification, each remaining powered up domain marks all current transfers to and from the effected domain as invalid, and ceases all further transmission to and receptions from the effected domain until the domain status signal DS indicates that power has been restored to the effected domain.

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For example, assume that first domain DM1 receives a first domain status signal DS1 that indicates that second domain DM2 has entered a low-voltage or lost power condition. In this case, first domain DM1 marks all current transfers to and from domain DM2 as invalid, and ceases all further transmission to and receptions from domain DM2 until

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domain status signal DS1 indicates that power has been restored to domain DM2.

When power again returns to a domain DM, domain notification circuit 110 again sends out the power on reset signal PR, followed a 5 predetermined time later by an updated domain status signal DS as described above. Thus, domain power notification system 100 insures that power is stable and sufficient before a domain powers up, and/or notifies other domains when a domain enters a low-voltage or lost power condition to prevent erroneous data from being transferred.

10 FIG. 2 shows a block diagram that illustrates a domain power notification system 200 in accordance with an alternate embodiment of the present invention. In the example shown in FIG. 2, system 200 includes a series of independently-powered domains DM1-DMn, and a corresponding series of independently-powered domain notification 15 circuits DNC1-DNCn that are connected to domains DM1-DMn.

As above, the domains DM1-DMn output a corresponding series of individual power supply signals PS1-PSn. The individual power supply signals PS1-PSn in this example are also the power supply voltages of the domains. As shown in FIG. 2, the domains DM1-DMn output power 20 supply signals PS1-PSn to the corresponding domain notification circuits DNC1-DNCn, and the other domains that communicate with the domains.

For example, assume that first domain DM1 communicates with only the fourth and fifth domains. In this case, first domain DM1 25 outputs power supply signal PS1 to domain notification circuit DNC1 (the corresponding domain notification circuit), and the domain notification circuits DNC in the fourth and fifth domains.

The domains DM1-DM_n also receive a series of domain status signals DS1-DS_n and a series of power on reset signals PR1-PR_n from the corresponding domain notification circuits DNC1-DNC_n. For example, first domain DM1 receives first domain status signal DS1 and a power on reset signal PR1 from domain notification circuit DNC1.

5 In addition, each domain DM includes internal logic that responds to the domain status signal DS and the power on reset signal PR. Further, each domain DM is connected to at least one other domain via a communication bridge 212. As above, communication bridge 212 can 10 be implemented with a number of serial connections between the domains, a parallel connection to the domains via a bus, or a combination of the two.

The domain notification circuits DNC1-DNC_n receive a number of signals, including reset signals RST1-RST_n. Reset signals RST1-RST_n 15 can be used to reset circuits DNC1-DNC_n and/or their corresponding domains DM via the power on reset signal PR.

The domain notification circuits DNC1-DNC_n also include low-voltage detection circuits LVD1-LVD_n that receive the individual power supply signals PS, and detect when the voltage of a power supply signal 20 PS of a domain DM rises above and falls below a minimum operating level of the domain DM. In addition, the low-voltage detection circuits LVD1-LVD_n output domain status signals DS1-DS_n to their corresponding domain DM. As above, a domain status signal DS indicates the power conditions of the relevant domains DM.

25 The domain status signals DS1-DS_n change if the power supply voltage of a domain DM that transmits data to, or receives data from, the domain DM rises above or falls below the minimum operating level. In addition, the low-voltage detection circuits LVD1-LVD_n can also

include signal filters FLT1-FLTn that remove the effect of voltage spikes, thereby preventing a low-voltage detection circuit LVD from responding to the transient power supply conditions that do not effect the operation of a domain.

5 In operation, each domain notification circuit DNC is a part of the first or one of the first domains to power up such as, for example, the input/output power domain of a semiconductor integrated circuit. After powering up, each domain notification circuit DNC monitors the power supply voltages of its corresponding domain DM. For example, first
10 domain notification circuit DNC1 monitors the power supply voltage of first domain DM1, while the nth domain notification circuit DNCn monitors the power supply voltage of the nth domain DMn.

Further, each domain notification circuit DNC detects when the power supply voltage provided to its corresponding domain DM rises
15 above a minimum operational voltage. When a domain notification circuit DNC detects via the power supply signal PS that the power supply voltage provided to its corresponding domain is above the minimum operational voltage, the domain notification circuit DNC outputs the corresponding power on reset signal PR. In response to the power on
20 reset signal PR, the corresponding domain DM powers up following a predefined sequence.

For example, when power supply signal PS1 indicates that the power supply voltage on first domain DM1 has risen above the minimum operational voltage, domain notification circuit DNC1 outputs power on
25 reset signal PR1 to first domain DM1. In response to power on reset signal PR1, first domain DM1 powers up following a predefined sequence. Thus, each domain notification circuit DNC insures that

stable power in excess of the minimum has been provided to its corresponding domain before the domain is powered up.

In addition, a predetermined time after outputting the power on reset signal PR, the domain notification circuit DNC outputs the domain status signal DS to its corresponding domain DM, while the domain notification circuits DNC that also received the power supply signal PS output the domain status signals DS to their corresponding domains. The domain status signals DS indicate the power status of the other domains.

10 For example, a predetermined time after outputting power on reset signal PR1, domain notification circuit DNC1 outputs the domain status signal DS1 to domain DM1, while the domain notification circuits DNC that also received the power supply signal PS1 output the domain status signals DS to their corresponding domains. Thus, once a domain powers up, the powered-up domains are informed of the other domains that are powered up and ready to communicate.

15 In addition to monitoring the power on reset sequence, or alternately from monitoring the reset sequence (each domain DM can alternately include a power on reset circuit that detects a stable voltage above the minimum and issues the reset command as well as receive a direct reset signal rather than via its corresponding LVD circuit), domain notification circuits DNC1-DNCn also monitor the domains DM1-DMn to detect low voltage and lost power conditions.

20 Each domain notification circuit DNC detects via the individual power supply signal PS that the power supply voltage provided to a domain DM has fallen below the minimum operational voltage of the domain DM. When this happens, the domain notification circuit DNC modifies the corresponding domain status signal DS to indicate that a

domain has fallen below the minimum required powered. Thus, once a domain loses power, the domains that are powered up are notified of this situation.

In response to the notification, each remaining powered up

5 domain DM marks all current transfers to and from the effected domain as invalid, and ceases all further transmission to and receptions from the effected domain DM until the domain status signal DS indicates that power has been restored to the effected domain.

For example, assume that first domain DM1 receives a first

10 domain status signal DS1 that indicates that nth domain DMn has entered a low-voltage or lost power condition via power supply signal PSn. In this case, first domain DM1 marks all current transfers to and from nth domain DMn as invalid, and ceases all further transmission to and receptions from nth domain DMn until domain status signal DS1

15 indicates that power has been restored to nth domain DMn.

When power again returns to nth domain DMn, domain

notification circuit DNCn again sends out the power on reset signal PRn, followed a predetermined time later by updated domain status signals DS1 and DSn as described above. Thus, domain power notification

20 system 200 insures that power is stable and sufficient before a domain powers up, and notifies other domains when a domain enters a low-voltage or lost power condition to prevent erroneous data from being transferred.

Thus, a domain power notification system that insures a proper

25 power up sequence, and notifies remaining domains when a connected domain has power problems has been described. In addition, the domain power notification system prevents erroneous data from being

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unknowingly received by one domain when another domain experiences a power condition.

It should be understood that various alternatives to the method of the invention described herein may be employed in practicing the
5 invention. Thus, it is intended that the following claims define the scope of the invention and that methods and structures within the scope of these claims and their equivalents be covered thereby.

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